

## **CYPRESS LAB CD-ROM CONSOLIDATES CYPRESS'S DEVELOPMENT TOOL OFFERINGS**

### **Cypress Offers Development Tools For Timing, Programmable Logic and USB ICs on a Single CD-ROM**

SAN JOSE, Calif., October 12, 2000 - Cypress Semiconductor (NYSE: CY) today announced availability of the Cypress Lab CD-ROM, consolidating development tools for its programmable logic, timing and USB ICs. Cypress now provides all of its development software in a single location, reducing overhead and simplifying development tool management for design engineers who use Cypress's solutions.

"We have found that an increasing number of design engineers are using devices from several different Cypress product portfolios, and the availability of a comprehensive development tool package simplifies system development efforts," said Scott Harmel, senior director for corporate marketing. "Cypress is unique in the semiconductor industry as the only company to include timing, programmable logic and USB devices in its portfolio, and we believe a unified software solution will help customers reduce system design cycles."

"The Cypress Lab CD-ROM is a collaborative product of the entire Cypress organization, transcending traditional product division lines," said Ralph Schmitt, vice president for marketing and sales. "This project reflects the company's recently-announced segment marketing initiative, a company-wide effort to align product portfolios with target market segments, rather than independent product line divisions. Cypress's segment marketing initiative will take advantage of the synergy among our product portfolios as they are designed into different types of applications. Cypress Lab is a whole product solution for designers using our IP. We believe this is a starting point for more IP tool integration, supporting designers in their drive for quick time-to-market using Cypress system solutions."

The Cypress Lab CD-ROM will include current versions of the following Cypress development tools:

Warp™ for programmable logic devices - With an installed base of over 26,000 seats, Warp is the semiconductor industry's leading HDL-based PLD design tool. Release 6.0 adds the Architecture Explorer and the Timing Analyzer, which were specifically designed to make Cypress's Delta39K™ CPLDs easier to use than FPGAs. Three editions - Warp, Warp Professional and Warp Enterprise - are included on the Cypress Lab CD-ROM.

ISR™ for programmable logic devices - The In-System Reprogramming (ISR) software is used to electrically reprogram Cypress CPLDs on a user's board through an IEEE-standard JTAG interface.

CYClocks™ for programmable frequency synthesizers - Version 3.64.02 of CYClocks provides the most accurate PMM calculations possible and includes Jedec support, a user-friendly interface, online help and documentation, and capabilities for power calculations. ·

Bitcalc for programmable frequency synthesizers - Bitcalc supports Cypress's earlier frequency synthesizers, and automatically recalculates the serial programming word to use for a required frequency.

CY3654 Development Kit for EnCoRe USB microcontrollers - The CY3654 Development

Kit Software provides the tools necessary to write and debug firmware for Cypress's EnCoRe USB microcontrollers, including the CYASM assembler, CYDB debugger for the CY3654 emulator board, sample firmware, and application notes. ·

Development Software Kit for EZ-USB® and EZ-USB FX™ Series devices - This kit includes an evaluation version of the Keil 8051 C Compiler, Assembler and Debugger, source firmware libraries, software utilities, and general-purpose device driver source code.

### **Pricing, Availability and Updates**

The Cypress Lab CD-ROM is shipping now with every version of Cypress's Warp programmable logic development kits. The Cypress Lab CD-ROM is now part of these kits for no additional charge and include all of the extra tools listed above. Warp development kits start at \$99. Going forward, Cypress Lab will be part of every software kit that Cypress sells, including programmable logic development systems, USB development kits, programming kits, and others. Future releases of Cypress Lab will coincide with new releases of the individual tools included on the CD-ROM. Upgrades of the individual tools will continue to be available on the Cypress web site: <http://www.cypress.com>.

### **About Cypress**

Cypress Semiconductor is "Driving the Communications Revolution"™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,100 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

*CyClocks, Delta39K, EZ-USB FX, ISR, Warp and "Driving the Communications Revolution" are trademarks of Cypress Semiconductor. EZ-USB is a registered trademark of Cypress Semiconductor.*